HV-EPICS Test Station Status Report
August 14, 2019

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1. Generated CAEN-SY4527 Test Results Power Point presentation

2. Calculated resistor value to be used in the load box, which will allow current test for CAEN-7030TN, A7435, and A1535 HV boards via developed CSS screens.

CAEN Board Model	Max Voltage [V]	Max Current [mA]	Max Power [w]	Selected Resistor [KΩ]	Max Voltage with Standard Resistor [V]	Max Power with Standard resistor [w]	Max Current with Standard Resistor [mA]
A1535	3500	3	8	2000	2200	2.42	1.1
A7435SN	3500	3.5	9	2000	2300	2.65	1.15
A7030TN	3000	1	1.5	2000	1720	1.48	0.86

3. Selected and ordered 38 Through Hole Resistor, 2 Mohm, Slim-Mox Series, 2.5 W, ± 1%, Radial Leaded, 20 kV.

4. Verified firmware version for hvcaentest1 CAEN mainframe
    4.1. PV and web server indicated version as 2.0.1

5. To continue with the debugging of hvcaentest2 mainframe CAEN software issues:
    5.1. Contacted CAEN representative to request firmware version 2.0.1 and enable the

    downgrade hvcaentest2 mainframe from its actual version 2.0.2 to version 2.0.1.

    5.2. CAEN representative informed that the soonest to get firmware version is for next week.

6. Conducted a meeting to present result for CAEN SY4527 systems tested and discuss about status of EPICS/Hardware test station developed by DSG and upcoming work.

7. For the development of an automated test for CAEN-SY4527 system, downloaded and installed CSS-Scan Server on dsg-b-linux1 PC.